

**IN THE CLAIMS**

Please cancel Claims 1-18 and add new Claims 19-38 as follows:

1-18. (Cancelled).

19. (New) For use in a wireless network comprising a plurality of base stations capable of communicating with a plurality of mobile stations in a coverage area of said wireless network, an apparatus for synchronizing a first one of said plurality of base stations comprising:

    a gigabit Ethernet transceiver capable of receiving a gigabit Ethernet data stream from a gigabit Ethernet network that transfers data between said plurality of base stations; and

    a clock recovery circuit capable of receiving said gigabit Ethernet data stream and generating therefrom a master clock signal, wherein said master clock signal is used to synchronize at least one of:

        a radio frequency (RF) transmitter portion of said first base station capable of transmitting a forward channel signal to a first one of said plurality of mobile stations; and

        a radio frequency (RF) receiver portion of said first base station capable of receiving a reverse channel from said first mobile station.

20. (New) The apparatus as set forth in Claim 19, wherein said master clock signal is used to synchronize at least one of:

an analog-to-digital converter (ADC ) circuit in said first base station; and  
a digital-to-analog (DAC) circuit in said first base station.

21. (New) The apparatus as set forth in Claim 20, wherein said master clock signal is used to synchronize a receiver local oscillator associated with said RF receiver portion of said first base station.

22. (New) The apparatus as set forth in Claim 21, wherein said master clock signal is used to synchronize a transmitter local oscillator associated with said RF transmitter portion of said first base station.

23. (New) The apparatus as set forth in Claim 22, wherein said master clock signal is used to synchronize an Ethernet receiver portion of said gigabit Ethernet transceiver.

24. (New) The apparatus as set forth in Claim 23, wherein said master clock signal is used to synchronize an Ethernet transmitter portion of said gigabit Ethernet transceiver.

25. (New) The apparatus as set forth in Claim 24, wherein said clock recovery circuit comprises a phase-locked loop capable of receiving said gigabit Ethernet data stream and generating said master clock signal.

26. (New) A wireless network comprising:

a plurality of base stations capable of communicating a plurality of mobile stations in a coverage area of said wireless network; and

a gigabit Ethernet network coupled to, and capable of transferring data between, said plurality of base stations, wherein a first one of said plurality of base station comprises:

a gigabit Ethernet transceiver capable of receiving a gigabit Ethernet data stream from said gigabit Ethernet network; and

a clock recovery circuit capable of receiving said gigabit Ethernet data stream and generating therefrom a master clock signal, wherein said master clock signal is used to synchronize at least one of:

a radio frequency (RF) transmitter portion of said first base station capable of transmitting a forward channel signal to a first one of said plurality of mobile stations; and

a radio frequency (RF) receiver portion of said first base station capable of receiving a reverse channel from said first mobile station.

27. (New) The wireless network as set forth in Claim 26, wherein said master clock signal is used to synchronize at least one of:

an analog-to-digital converter (ADC) circuit in said first base station; and

a digital-to-analog (DAC) circuit in said first base station.

28. (New) The wireless network as set forth in Claim 27, wherein said master clock signal is used to synchronize a receiver local oscillator associated with said RF receiver portion of said first base station.

29. (New) The wireless network as set forth in Claim 28, wherein said master clock signal is used to synchronize a transmitter local oscillator associated with said RF transmitter portion of said first base station.

30. (New) The wireless network as set forth in Claim 29, wherein said master clock signal is used to synchronize an Ethernet receiver portion of said gigabit Ethernet transceiver.

31. (New) The wireless network as set forth in Claim 30, wherein said master clock signal is used to synchronize an Ethernet transmitter portion of said gigabit Ethernet transceiver.

32. (New) The wireless network as set forth in Claim 31, wherein said clock recovery circuit comprises a phase-locked loop capable of receiving said gigabit Ethernet data stream and generating said master clock signal.

33. (New) The wireless network as set forth in Claim 32, wherein said gigabit Ethernet

data stream is transmitted by a second of said plurality of base stations, wherein said second base station comprises a global positioning system (GPS) receiver that synchronizes transmission of said gigabit Ethernet data stream.

34. (New) For use in a wireless network comprising a plurality of base stations capable of communicating with a plurality of mobile stations in a coverage area of the wireless network, a method of synchronizing a first one of the plurality of base stations comprising the steps of:

receiving in a gigabit Ethernet transceiver of the first base station a gigabit Ethernet data stream from a gigabit Ethernet network that transfers data between the plurality of base stations; and

receiving the gigabit Ethernet data stream in a clock recovery circuit of the first base station and generating therefrom a master clock signal; and

at least one of:

synchronizing a radio frequency (RF) transmitter portion of the first base station with the master clock signal; and

synchronizing a radio frequency (RF) receiver portion of the first base station with the master clock signal.

35. (New) The method as set forth in Claim 34, further comprising at least one of the steps of:

synchronizing an analog-to-digital converter (ADC) circuit in the first base station with the

master clock signal; and

    synchronizing a digital-to-analog (DAC) circuit in the first base station with the master clock signal.

36. (New) The method as set forth in Claim 35, further comprising the step of synchronizing a receiver local oscillator associated with the RF receiver portion of the first base station.

37. (New) The method as set forth in Claim 36, further comprising the step of synchronizing a transmitter local oscillator associated with the RF transmitter portion of the first base station.

38. (New) The method as set forth in Claim 37, further comprising at least one of the steps of:

    synchronizing an Ethernet receiver portion of the gigabit Ethernet transceiver; and  
    synchronizing an Ethernet transmitter portion of the gigabit Ethernet transceiver.